

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s):

Richard K. SITA et al.

Title:

APPARATUS FOR HIGH DATA RATE SYNCHRONOUS INTERFACE

AND METHOD THEREOF

App. No.:

09/657,255

Filed:

09/07/2000

Examiner:

TRUJILLO, James K.

Group Art Unit:

2116

Atty. Dkt. No.: 1376-0001410

Mail Stop ISSUE FEE Commissioner for Patents PO Box 1450 Alexandria, VA 22313-1450

AMENDMENT AFTER ALLOWANCE (37 C.F.R. § 1.312)

Dear Sir:

This Amendment is being submitted following the Notice of Allowance mailed on January 19, 2005, and prior to payment of the issue fee.

CERTIFICATE OF TRANSMISSION/MAILING

I hereby certify that this correspondence is being facsimile transmitted to the USPTO or deposited with the United States Postal Service with sufficient po addressed to the Commissioner for Petents on first class mail, in an envelope

Judy Carey

Typed or Printed Name

IN THE TITLE

The Title has been amended to read:

APPARATUS FOR HIGH DATA RATE SYNCHRONOUS INTERFACE AND A METHOD THEREOF-USING A DELAY LOCKED LOOP TO SYNCHRONIZE A CLOCK SIGNAL AND A METHOD THEREOFAT AN ENDPOINT